



Attorney's Docket No. 4990 USA/W-C/W-C/JB1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)	
Steven Verhaverbeke et al.)	Examiner: Umez-Eronini, Lynette T.
Application No.: 09/891,730)	Art Unit: 1765
Filed: June 25, 2001)	
For: CLEANING METHOD AND SOLUTION)	
FOR CLEANING A WAFER IN A)	
<u>SINGLE WAFER PROCESS</u>)	
Mail Stop RCE		
Commissioner for Patents		
P.O. Box 1450		
Alexandria, Virginia 22313-1450		

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Sir:

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search

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Claire Walters 7/22/2004
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has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

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 A statement pursuant to 37 C.F.R. §1.97(e) or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7/22 2004

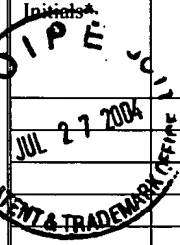


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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known Application Number: 09/891,730 Filing Date: June 25, 2001 First Named Inventor: Steven Verhaverbeke Art Unit: 1765 Examiner Name: Umez-Eronini, L. Attorney Docket Number: 4990 USA/W-C/W-C/JB1 BSTZ Docket No.: 4887.P447		
Sheet	1	of	2		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)				
		US-	3,045,702	07-24-1962	Nakata	
		US-	3,291,347	12-13-1966	Blades	
		US-	4,243,071	01-06-1981	Shackelford	
		US-	4,554,050	11-19-1985	Minford et al.	
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		US-	5,962,384	10-05-1999	Cooper et al.	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
		EP 0 245 667 A1		11-19-1987	Edeleanu Gesellschaft mbH		No

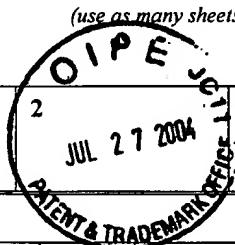
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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
		Application Number	09/891,730
		Filing Date	June 25, 2001
		First Named Inventor:	Steven Verhaverbeke
		Art Unit	1765
		Examiner Name	Umez-Eronini, L.
Sheet 2 of 2	Attorney Docket Number BSTZ Docket No.		4990 USA/W-C/W-C/JB1 4887.P447



NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		Chapter 3 – Silicon Wafer Cleaning Procedure, http://www-mtl.mit.edu/CAFE/sop_copy/rca.html , 04/24/2002, 4 pages.	
		Patent Abstracts of Japan , Vol. 004, No. 089 (C-016), June 25, 1980 & JP 55 051427 A (Sakaoka Kazuhiko), April 15, 1980, 1 page.	
		Silicon VLSI Technology, Fundamentals, Practice and Modeling, By Plummer, Deal and Griffin, IC Manufacturing-Chapter 4, Semiconductor Manufacturing-Clean Rooms, Wafer Cleaning And Gettering- Chapter 4, ©2000 by Prentice Hall, Upper Saddle River, N.J., 16 pages.	
		Written Opinion for PCT/US/01/41160 mailed April 2, 2004, 7 pages.	
		2 nd Annual International SEMATECH Wafer Cleaning and Surface Preparation Workshop 2000, April 11-12, 2000, Hyatt Hotel, Austin, TX, 24 pages.	

Examiner Signature		Date Considered	
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